

## APPLICATION DATA SHEET

### APPLICATION INFORMATION

Filing Date:: 11-18-2003  
Application Type:: Regular  
Subject Matter:: Utility  
Title:: Method and Apparatus for Providing  
High Common-Mode Rejection Ratio in  
a Single-Ended CMOS Operational  
Transconductance Amplifier  
Attorney Docket Number:: X-1442 US  
Request for Early Pub?:: No  
Request for Non-Pub?:: Yes  
Total Drawing Sheets:: 06  
Small Entity?:: No  
Petition included?:: No

### APPLICANT INFORMATION

Applicant Authority Type:: Inventor  
Primary Citizenship Ctry:: Ukrainian  
Status:: Full Capacity  
Given Name:: Maxim  
Family Name:: Pribytko  
Street:: 15 Wheaton Court  
City:: Inchicore Terrace North  
State or Province:: Inchicore, Dublin 8  
Country:: Ireland  
  
Applicant Authority Type:: Inventor  
Primary Citizenship Ctry:: Ireland  
Status:: Full Capacity  
Given Name:: Patrick  
Middle Name:: J.  
Family Name:: Quinn  
Street:: 15 Monastery Heath Green

City:: Clondalkin  
State or Province:: Dublin 22  
Country:: Ireland

**CORRESPONDENCE INFORMATION**

Correspondence Customer Number:: 24309

**REPRESENTATIVE INFORMATION**

<b>Representative Customer Number::</b>	<b>24309</b>	
---	--------------	--

**ASSIGNEE INFORMATION**

Assignee Name:: Xilinx, Inc.  
Street:: 2100 Logic Drive  
City:: San Jose  
State or Province:: California  
Postal or Zip Code:: 95124